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September 9, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attorney Docket No.: 004-8841

Transmitted herewith for filing is a patent application as follows:

Inventor(s): Sriram Satakopan, Arvindvel Shanmugavel, Shunjiang Xu, Von-Kyoung Kim and Peter Lai
Title: LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE
Assignee: Sun Microsystems, Inc.

Enclosed are:

- Application Data Sheet (4 page(s))
- Request for Non-Publication and Certification under 35 U.S.C. 122(b)(2)(B)(i) (1 page(s))
- 12 Pages of Written Description (including Specification, Claims and Abstract)
- 2 Sheets of Drawings, X Formal / Informal
- Declaration for Patent Application (2 pages), X Executed/ Unexecuted
- Assignment of the Invention (3 pages, including Cover Sheet)
- Information Disclosure Statement (____ pages)
 - with Form(s) PTO 1449 (____ page(s)) and copies of ____ reference(s)
- Other: Check in the amount of \$1,012
- This Transmittal Letter (in duplicate) (2 page(s)) Return Postcard

CLAIMS AS FILED

	Number Filed	Number Extra	Rate	Fee
Basic Fee =				750.00
Total Claims	23 - 20	= 3	x \$18.00 =	54.00
Independent Claims	5 - 3	= 2	x \$84.00 =	168.00
Multiple Dependent Claims (if any) - \$280.00 fee				
Other: Record Assignment				40.00
TOTAL FILING FEE				\$1,012.00

- Small entity status is entitled to be asserted for the application.
- A check is enclosed for the Total Filing Fee shown above.
- Please charge the Total Filing Fee shown above to Deposit Account 50-0631.
- The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 or 1.17 that may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.

03945 U.S. PTO
10/657964



ZAGORIN, O'BRIEN & GRAHAM, L.L.P.

September 9, 2003

RE: 004-8841

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EXPRESS MAIL LABEL NO.
EV335379281US

Respectfully submitted,


Nicole Teitler Cave, Reg. No. 54,021
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sriram Satakopan, Arvindvel Shanmugavel, Shunjiang Xu, Von-Kyoung Kim and Peter Lai

Title: LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE

Application No.: (unassigned)

Filed:

Herewith

Examiner: (unassigned)

Group Art Unit:

(unassigned)

Atty. Docket No.: 004-8841

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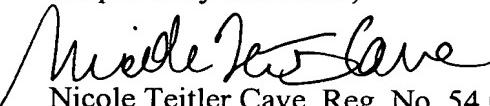
**REQUEST FOR NONPUBLICATION
AND CERTIFICATION (35 U.S.C. § 122(b)(2)(B)(i))**

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. § 122(b).

EXPRESS MAIL LABEL NO.:

EV335379281US

Respectfully submitted,


Nicole Teitler Cave, Reg. No. 54,021
Attorney for Applicant(s)
(512) 347-9030
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This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(ii)).